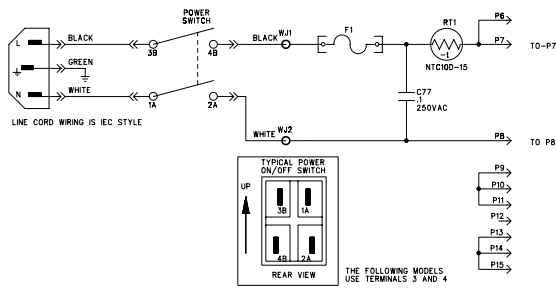


REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
A	PR558	21-JUL-03	MCU

MODELS:	F1 VALUE
100-120V	125V F4A
230-240V	250V F1.25A



- 6. AC AND DC VOLTAGES READ TO GROUND WITH A DVM UNDER THE FOLLOWING CONDITIONS:
 UNIT AT RATED LINE VOLTAGE
 NO INPUT SIGNAL (TP1-TP6, TP20-TP29 ONLY)
 VOLTAGES MAY VARY +/- 20%
- ADDITIONAL CONDITIONS FOR TP7-TP9:
 300 OHM SINEWAVE INPUT SIGNAL AT J1
 4 OHM RESISTIVE LOAD AT TP7-TP9
 OR SELECT (S2) DRIVE SELECT (S1) OUT YELLOW DRIVE CONTROL AT 3 REVERSE CONTROL AT 1
 ALL OTHER CONTROLS AT 50-1/2 (CLOCK ROTATION)
- 5. THIS SCHEMATIC IS FOR PDB FABRICATION P/N 0054281000 AND PCB ASSSEMBLY P/N 0054282000
- 4. ALL DIODES ARE IN4004.
- 3. ALL POLARIZED CAPACITORS IN UF, 20% SOV MINIMUM.
- 2. ALL UNPOLARIZED CAPACITORS IN UF, 10% OR BETTER; 50V MINIMUM POWER SUPPLY BYPASS CAPACITORS ARE 20%.
- 1. ALL RESISTORS IN OHMS, 5% 1/4W.

THIS DOCUMENT CONTAINS INFORMATION OF A PROPRIETARY NATURE TO FENDER MUSICAL INSTRUMENTS AND IS SUBMITTED TO YOU IN CONFIDENCE AND SHALL NOT BE DISCLOSED OR TRANSMITTED TO OTHERS WITHOUT AUTHORIZATION FROM FENDER MUSICAL INSTRUMENTS.

CHECKED BY: _____ TITLE: SERVICE DIAGRAM, COMBINED (schematic)
 DATE: _____

APPROVED BY: _____ SIZE: DRAWING NUMBER
 DATE: _____ D 0061536000
 DATABASE FILE: Z5588.SCH ENGR: MULRICH
 RELEASE DATE: 21-JUL-03 SHEET: 1 OF 2

OUTPUT POWER:
 ADJUST 100HZ INPUT SIGNAL TO VERIFY SO WATS RMS AT 5% THD INTO 4 OHM RESISTIVE LOAD AT RATED LINE VOLTAGE